DS_DMA - Bug # 50: Changed DMA_REQUEST between TEST_CHECK and TEST_GENERATE

Status:	Resolved	**Priority:**	Normal
Author:	Dmitry Smekhov	**Category:**	
Created:	05/12/2013	**Assignee:**	
Updated:	06/23/2013	**Due date:**	06/23/2013
Subject:	Changed DMA_REQUEST between TEST_CHECK and TEST_GENERATE		
Description:	In the document:		
	TEST_CHECK - dmar0		
	TEST_GENERATE - dmar1		
	In the FPGA incorrect:		
	TEST_CHECK - dmar1		
	TEST_GENERATE - dmar0		

History

06/23/2013 10:40 pm - Dmitry Smekhov

- Due date set to 06/23/2013

- Status changed from New to Resolved

- % Done changed from 0 to 100