

## DS\_DMA - Development # 16: ДҮЊЄД³¼ДµД°Њ, SP605\_LX45T\_CORE

<b>Status:</b>	Closed	<b>Priority:</b>	Normal
<b>Author:</b>	Dmitry Smekhov	<b>Category:</b>	
<b>Created:</b>	09/02/2011	<b>Assignee:</b>	
<b>Updated:</b>	09/28/2011	<b>Due date:</b>	09/28/2011
<b>Subject:</b>	ДҮЊЄД³¼ДµД°Њ, SP605_LX45T_CORE		
<b>Description:</b>	ДҮЊЄД³¼ДµД°Њ, Д°Д³¼Д½Њ,ЊЄД³¼Д»Д»ДµЊЄД° PCI Express x1 Д´Д»Њ• Д¼Д³¼Д´ЊfД»Њ• SP605. ДҮД»Д´Дј; Xilinx Spartan-6 LX45T.		

### History

#### 09/02/2011 04:41 pm - Dmitry Smekhov

- % Done changed from 0 to 50

Д' Spartan-6 Д½ДµЊ, Д±Д»Д³¼Д°Д³¼Д² DSP. Д•Њ•Њ,ЊЄ DSP48A1. ДšД³¼Д¼Д¼Д¼Д³¼Д½ДµД½Њ,Њ( ctrl\_ext\_descriptor, ctrl\_ram\_cmd Д² Д·Д°Д²Д,Њ•Д,Д¼Д³¼ДЊ•Њ,Д, Д³¼Њ, Д¼Д°ЊЄД°Д¼ДµЊ,ЊЄД° is\_dsp48 Д,Њ•Д¼Д³¼Д»ЊЄД·ЊfЊЗЊ, Д»Д,Д±Д³¼ Д±Д»Д³¼Д°Д, DSP48, Д»Д,Д±Д³¼ Д»Д³¼Д³Д,Д°Њf. ДҫЊЄДµД±ЊfДµЊ,Њ•Њ• Д¼ЊЄД³¼Д²ДµЊЄД,Њ,ЊЄ ЊЄД°Д±Д³¼Њ,Њf Д±ДµД· Д,Њ•Д¼Д³¼Д»ЊЄД·Д³¼Д²Д°Д¼Д½Д,Њ• DSP48

#### 09/28/2011 04:37 pm - Dmitry Smekhov

- Due date set to 09/28/2011

- Status changed from Confirmed to Closed

- % Done changed from 50 to 100

Д'ДµЊЄЊ•Д,Њ• 1.0 Д·Д°Д²ДµЊЄЊ`ДµД½Д°.